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TRANSMITTAL FORM

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Total Number of Pages in This Submission

2

Application Number	10/010,343
Filing Date	December 5, 2001
First Named Inventor	Visvamohan Yegnashankaran et al.
Group Art Unit	2826
Examiner Name	L. Andujar

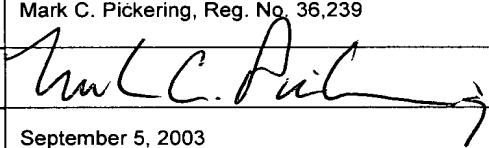
Attorney Docket Number

100-16300 (P05089)

ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment/Response	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final (Response)	<input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Inquiry
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	Return Receipt Postcard
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<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> CD, Number of CD(s) _____	Establishment of Right of Assignee to Prosecute Under 37 C.F.R. § 3.73(b)
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application	Remarks	
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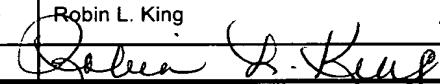
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Mark C. Pickering, Reg. No. 36,239
Signature	
Date	September 5, 2003

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
APPLICATION FOR UNITED STATES PATENT

In re Patent Application of) Group Art Unit: 2826
Visvamohan Yegnashankaran et al.)
Appln. No. 10/010,343) Examiner: L. Andujar
Filed: December 5, 2001)
For: INTEGRATED CIRCUIT AND METHOD) ESTABLISHMENT OF RIGHT OF ASSIGNEE
OF FORMING THE INTEGRATED) TO PROSECUTE UNDER 37 C.F.R. §3.73(b)
CIRCUIT HAVING A DIE WITH HIGH Q)
INDUCTORS AND CAPACITORS)
ATTACHED TO A DIE WITH A CIRCUIT)
AS A FLIP CHIP)
)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

NATIONAL SEMICONDUCTOR CORPORATION is the assignee of the entire right, title and interest in the above-identified application by assignment dated November 29, 2001 and November 28, 2001, **which assignment is recorded in the U.S. Patent and Trademark Office at Reel 012373, Frame 0443.**

The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

Dated: 8/25/03

By: 

Name: Christopher J. Byrne

Title: Director of Intellectual Property and Technology
Licensing

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